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## \*\* CONTINUING DATA \*\*\*\*\*

This application is a DIV of 08/784,294 01/16/1997 PAT 6,100,562

SWC

## \*\* FOREIGN APPLICATIONS \*\*\*\*\*

JAPAN 8-088759 03/17/1996

SWC

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 05/21/2000

Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS DRAWING	TOTAL CLAIMS	INDEPENDENT CLAIMS
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	6	1	1
Verified and Acknowledged	<i>Frank Gove Snel</i> Examiner's Signature	Initials			

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## TITLE

Method of manufacturing a semiconductor device

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